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LIST OF PRIOR ART CITED BY APPLICANT (use as many sheets as necessary)				Application Number	10/612,479		
				Filing Date	July 2, 2003		
				First Named Inventor	Esin Terzioglu et al.		
				Group Art Unit			
				Examiner Name			
Sheet 1		of 1		Attorney Docket Number	13464US04		
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C1)	A	KIYOO ITOH et al., <i>Trends in Low-Power RAM Circuit Technologies</i> , Proceedings of the IEEE, Vol. 83, No. 4, pages 524-543, April 1995					
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